



Docket No. 10001.000606  
Response To Restriction Requirement  
October 25, 2002

#12/ ELECTION/A

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Craig L. Stevens et al.

Serial No. 09/872,796

Examiner: Timothy J. Sutton

Filing Date: 06/01/2001

Art Unit: 2813

Title: Architecture For High Throughput Semiconductor Processing Applications

Honorable Commissioner of Patents  
Washington, D.C. 20231

**RESPONSE TO RESTRICTION REQUIREMENT AND AMENDMENT**

Sir:

This submission is responsive to the Office Action mailed on October 2, 2002  
("last Office Action").

Please amend the above-identified application as follows:

**In The Claims:**

Please cancel claims 14-16 and 22-24 without prejudice.

**REMARKS**

In response to the Restriction Requirement of the last Office Action, Applicants hereby elect claims 1-13 and 17-21 (Group I) for examination. Claims 14-16 and 22-24 have been accordingly cancelled to expedite allowance of the remaining claims.

Favorable action is respectfully solicited.

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